

# Review of Coupled Two and Three Phase Interleaved Boost Converter (IBC) and Investigation of Four Phase IBC for Renewable Application

P.Abishri\*, Umashankar S\*, Sudha Ramasamy\*‡

\*School of Electrical Engineering, VIT University, Vellore, 632014, India

(abishri.p2015@vit.ac.in, umashankar.s@vit.ac.in, sudha.r@vit.ac.in)

‡Corresponding Author; Tel: +91 9578409526, sudha.r@vit.ac.in

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**Abstract-** The power level of a power electronic converter is limited due to several factors, increase in current causes an increase in the stress on switching devices. Besides, the diode reverse recovery current and parasitic resonance current become greater than the main switch can handle. Hence, the size of the boost inductor should be increased to avoid saturation and overheating problems. In order to advance the power level significantly the methods, including device paralleling, module paralleling and interleaving are widely utilized. For some applications, boost stages are designed modularly such that the converter stages can be connected in parallel to meet the increasing power requirement. This method is preferable as it is easy to increase the power rating by simply stacking converters with increased redundancy. The drawbacks of the method are it's relatively high cost, large volume covered, and cooling difficulties. Furthermore, to provide equal sharing of input current among the converters, additional circuitry should be utilized and the currents of individual converters do not return properly, current of one module can circulate through other module and some unexpected failures may occur. This paper reviews the ripple input current and output voltage of two and three phase Interleaved Boost Converter (IBC) and investigates the performance of four phase IBC for renewable applications.

**Keywords-** Renewable Energy Systems, Solar Photo Voltaic, Wind Turbine Generator Artificial Neural Network, Phase Shift Modulation, Interleaved Boost Converter, Hybrid Power System.

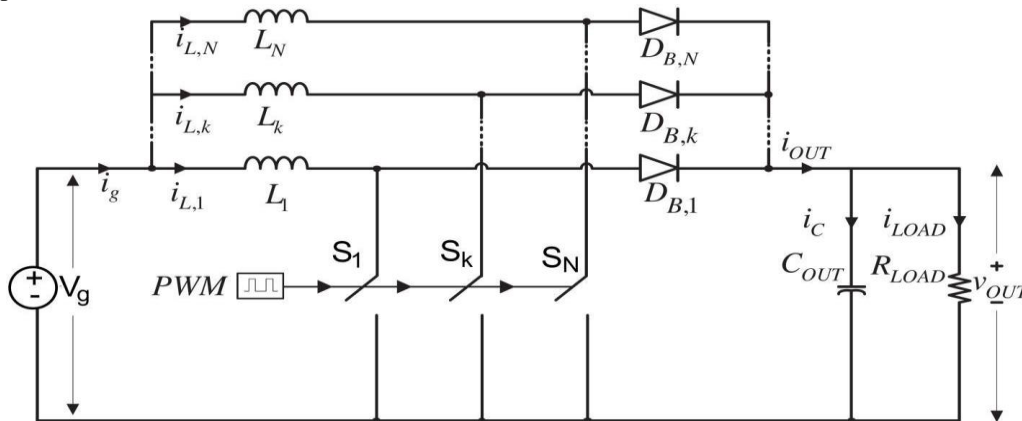
## 1. Introduction

Generating electricity from systems based on Renewable Energy Systems (RES) applications like Photo Voltaic (PV) cell, fuel cell, wind etc are one of the reliable remedies to conserve energy [1]. For PV cell, PV model [2-5] is generated and analyzed in conjunction with power electronic switches for a maximum power point tracker [6-7] [63]. While considering fuel cells [8], it has been used as the phenomenon sources of distributed energy. Because of the high efficiency, low environmental impact and scalability [9], the fuel cell based supply system converts its generated low dc voltage using power conditioner [10]. Hence it is used for residential application. The battery model system is implemented using effective equivalent circuit model structure featuring lead-acid batteries [11-13]. Diesel Wind Turbine is another application of RES which satisfies the power demand [14]. These are superior when compared to the conventional sources like fossil fuels, RES will fulfill the world's energy demand. [15] Novel ideas from various studies proposes the parallel switching of devices are used widely in telecommunication industry and operated under closed loop to regulate the output voltage which reduces current

stress and maximize its performance [16-18]. Paralleling two or more switching devices is a widely utilized approach to increase the current handling capability of switches. The advantages of the parallel converters scheme is to implicit proper design, dynamic response, robustness and tight steady state [19-21]. In high power high density converters, the power MOSFETs is being used in parallel as the main switch to meet the current rating requirement, also it increase the switching frequency and reduce the power loss. Proper gating by PWM signal is applied to devices in order to reduce switching losses and switch currents are shared [22]. This method is useful in devices with positive temperature constant (PTC) such as MOSFETs. The device paralleling method is not practical for all applications. The power stage of the converter consists of semiconductor switches and magnetic components. The drawback such as unbalanced current sharing between semiconductor switches and magnetic saturation is partially overcome by the power stage paralleling method [23] shown in Fig.1.

In this configuration, transistors are not paralleled directly. Therefore current sharing problems mentioned previously are not an issue. Besides, energy storage requirement of the inductors is decreased, so the total magnetic component volume can be reduced

significantly. Furthermore, hot swapping and increasing the power rating by simply stacking modules are possible.



**Fig.1.** Boost rectifier with N-phase paralleled power stage

Power Stage paralleling is very practical, but it is not the optimal solution in terms of converter performance and size considerations. The same paralleling method can be utilized with a different switching pattern than the identical switching patterns in order to reduce input current and output voltage ripples. The separate power branches are controlled by interleaved switching signals but they have a phase shift. In this review RES such as SPV, wind turbine generator, battery and fuel cell is taken for study and analysis [24].

### 1.1. Interleaved Boost Converter

During the last few decades, power electronics research has focused on the development of multi-phase parallel DC-DC converters. It is useful to obtain the regulated output voltage from several input power sources such as a solar array, wind generator, fuel cell [26]. Among the various topologies, interleaved boost converter (IBC) is considered as a better solution for fuel cell systems[65-66], due to improved electrical performance, reduced weight and size[66]. This provides positive output voltage without any additional transformer and it is capable of bidirectional operation, increase the power processing capability and improves the reliability of the power electronic system[25]. While comparing conventional single input converters, this topology minimizes the total number of components [27] and has simple circuit structure [28].

### 1.2. Advantages of Interleaved Boost Converter

The advantages of constructing a power converter by means of interleaved parallel connected converters [29] are ripple cancellation in both the input and output waveforms to a maximum extent, and lower value of ripple amplitude and higher ripple frequency in the resulting input and output waveforms [30-31]. By splitting the current into many power paths, conduction losses ( $I^2R$ ) can be reduced, increasing overall efficiency [32]. Multi-phase interleaved boost converter created by

paralleling several phase legs and inductors to share the input current. Main asset of this configurations is to increase the power quality of the converter and the input current ripple is significantly reduced with the increase in number of phases[33]. Designing converter with very stringent power quality, high current, low harmonic distortion [34] requires this configuration. Increasing the phase inductance is more essential in interleaved configuration [33].

Mathematically there is no limit for the number of interleaved power branches. But in practice as the phase number increases, the system complexity increases and maintenance becomes difficult. The input/ EMI filter size and output capacitor size are reduced in proportion with the ripple reduction [35-36]. The disadvantage of the interleaving method is the increase in the gate driving logic complexity, but necessarily the size and cost of the gate drive [35]. Logic signals to all the gates are equally phase shifted by the amount defined in (1).

$$\text{Phase shift} = K \cdot 2/N \quad \text{Eq. (1)}$$

In Equation (1), 'N' denotes the number of interleaved branches and 'k' denotes the order of discrete interleaved branches ( $k = 1, \dots, N$ ). Coupled inductor is one of the main components in power electronics circuits, which plays an important role in DC/DC converters [37]. From the literature review, the various configurations of the IBCs such as uncoupled, directly coupled and inversely coupled it is reported that the directly coupled IBC [42][62][86] gives a reduced input current ripple[40], output voltage ripple, responds fast.[39] than the other two systems and it is best suited for renewable energy applications[15][38]. The soft switching dc-dc converter with the coupling inductor suppress the overvoltage [41].

**2. IBC as the Basic Converter Unit of HPS**

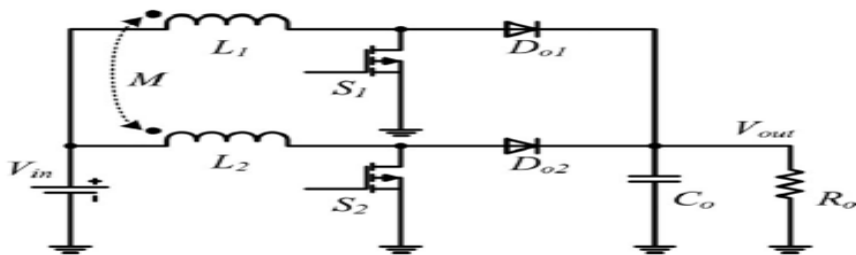
The RES, such as SPV panel [4][6] and fuel cell [43-45] generally have low output voltage which mandates the use of the boost converter to increase and match with the load voltage. But the use of conventional boost converters generally introduce large amounts of ripple content in the input current as well as in the output voltage. It inherently shortens the lifetime of RES sources such as SPV panel and fuel cell and also decreases the performance of the sources. With large input current ripple and output voltage ripple, the control of the system parameters such as output voltage control, power flow control becomes difficult as it mandates a sturdy averaging circuit for each input parameter of the controller. An input output magnetically coupled interleaved converter smoothen the operating modes and improves the inner dynamics and fuel cell application.

Based on the analysis performed it is proved that the IBC offers better input current ripple reduction and the output voltage ripple reduction.[15][17][29][31] Hence, in view of the advantages offered[32-36] [46], the IBC is recommended as the basic converter unit in the proposed Hybrid Power System.

*2.1. Ripple Analysis in 2-Phase IBC*

In a two-phase converter, there are two output stages that are driven 180 degrees out of phase. By splitting the current into two power paths, conduction ( $I^2R$ ) losses can be reduced, increasing overall efficiency compared to a single-phase converter. Because the two phases are combined at the output capacitor, effective ripple frequency is doubled, making ripple voltage reduction much easier. Likewise, power pulses drawn from the input capacitor are staggered, reducing ripple current requirements. To overcome these limitations multiphase interleaving technique is used [47].

Multiphase dc-dc converters are widely used in high-power applications[65] ranging from automotive to distributed generation [56][59-60][81] compared to single phase converters[48]. This topology uses a coupled inductor in the place of main inductors of the conventional IBC [48][51][69]. By coupling the main inductors, the input current ripple and switching loss[58][64][67-68] can be reduced more further[38][48][53], improves the dynamic performance[52], efficiency[65][69], switching speed[49] and output voltage[54]. Moreover the power density can be easily achieved because there is only one core adopted[50][62]. In case of high output power and low output voltage it is typically beneficial to use paralleled converters[61]. The circuit of the coupled inductor is shown in Fig.2



**Fig.2.** Schematic of coupled inductor IBC

**Table 1.** Input current ripple of the 2-phase IBC connected to SPV panel and WTG

Parameter (N=2)	SPV panel			WTG		
	$I_{Min}$ (A)	$I_{Max}$ (A)	$I$ (A)	$I_{Min}$ (A)	$I_{Max}$ (A)	$I$ (A)
Input current ripple in Inductor 1	4.01	4.18	.17	4.22	4.39	0.17
Input current ripple in Inductor 2	4.05	4.24	.19	4.17	4.35	0.18
Input source current Ripple	8.26	8.34	.08	8.62	8.71	0.09

**Table 2.** Input current ripple of the 2-phase IBC connected to the Fuel cell and Battery

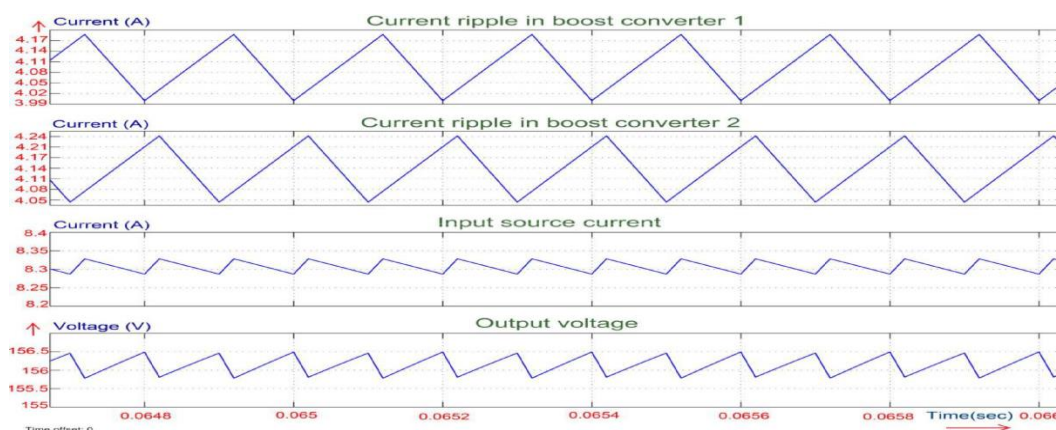
Parameter (N=2)	Fuel cell			Battery		
	$I_{Min}$ (A)	$I_{Max}$ (A)	$I$ (A)	$I_{Min}$ (A)	$I_{Max}$ (A)	$I$ (A)
Input current ripple in inductor 1	4.19	4.37	0.18	4.08	4.25	0.17
Input current ripple in inductor 2	4.13	4.32	0.19	4.09	4.27	0.18
Input source current ripple	8.38	8.46	0.08	8.28	8.36	0.08

Table 2 and Table 3 shows the input ripple current across inductor 1 and 2 and the input source current in SPV, WTG, fuel cell and battery, where  $I_{Min}$  and  $I_{Max}$  are the minimum and maximum current across the inductors.

Table 2.2 shows the output ripple voltage of the 2 phase IBC for all the RES. Figure 3 shows the corresponding wave form.

**Table 3.** Output voltage ripple of the 2-phase IBC connected to RES's

Parameter (N=2)	Fuel cell			Battery		
	$I_{Min}$ (A)	$I_{Max}$ (A)	$I$ (A)	$I_{Min}$ (A)	$I_{Max}$ (A)	$I$ (A)
Input current ripple in Inductor 1	4.19	4.37	0.18	4.08	4.25	0.17
Input current ripple in Inductor 2	4.13	4.32	0.19	4.09	4.27	0.18
Input source current Ripple	8.38	8.46	0.08	8.28	8.36	0.08



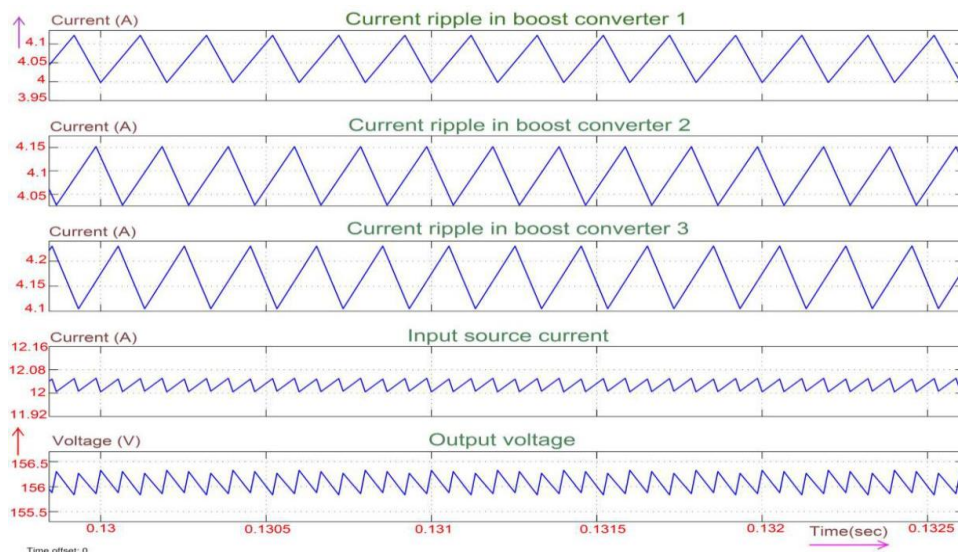
**Fig.3.** Current and voltage output of SPV panel fed 2-phase IB

### 3. Ripple Analysis in 3-Phase IBC

A three-phase high power dc/dc converter with an active clamp is capable of increased power transfer due to its three-phase power configuration, and it reduces the rms current per phase which is imposed into fuel cells thus reducing conduction losses compared to other conventional boost converters[84-85]. Parallel control method of three-phase interleaved dc-dc converters can be used for the battery test system and it is used for improving the unbalance factor. Here the current sharing control method is used for maintaining dc-dc converter current equal [70]. Three phase interleaved DC-DC converter[82] can be connected with electric vehicle to operate motors and inverters[71]. The input ripple current is minimized by three phase interleaved operation and the transformer turn ratio is reduced by its inherent boost mode operation effectively[72]. The interleaved structure of the current source port can provide the desired small current ripple to benefit the PV panel to achieve the maximum power point tracking (MPPT). The MPPT and power flow regulations are realized by duty cycle control and phase-shift angle control, and the zero-voltage switching can be guaranteed in the PV application even when the dc-link voltage varies[73]. The input ripple current is minimized by three phases interleave operation, and the transformer turn ratio is reduced by its inherent boost mode operation effectively [74][77]. The battery is

connected to the three-phase interleaved dc-dc converter in order to reduce the ripple current from 32.5% to 8%, increase of battery lifetime and reduction of total size of inductors. The ripple current[79] is further reduced to 2% from 8% by connecting a filter capacitor and design rule of filter capacitor is analyzed[75].

Also it reduces the core number[77], voltage stress[83], improves efficiency[79][81] by integrating three input inductors into a single core with coupling to achieve zero-voltage-switching (ZVS) over a wide range by hybrid pulse-width-modulation (PWM) and phase-shift-modulation (PSM) control. Single-switch boost stages, connected between three-phase rectifiers and DC link capacitors, allow good power factor correction when operated in the discontinuous conduction mode[80]. This converter has a great potential application in fuel cell vehicle and distributed power generation [76]. The pulsation power decoupling control method can be used to minimize the dc capacitor with low cost[78]. The output of the 3-phase IBC connected to WTG is shown in Fig.3.1. The magnitude of current ripple in the inductors ( $I_{L1}, I_{L2}, I_{L3}$ ) and source current and output voltage ripple of the 3-phase IBC's connected to SPV panel, WTG, fuel cell and battery are shown in table 4, 5 & 6. and corresponding waveforms is shown in figure 4



**Fig.4.** Current and voltage output of WTG fed 3-phase IBC

### 4. Ripple Analysis in 4-Phase IBC

The conventional boost converters has large size of the storage capacitor on the dc link and suffers from the disadvantage of discontinuous current injected to the load.

The multi-phase operation of boost converter overcomes this disadvantage with appropriate phase shift in the control circuit of main switches.

**Table 4.** Input current ripple of the 3-phase IBC connected to SPV panel and WTG

Parameter (N=3)	SPV panel			WTG		
	$I_{Min}$ (A)	$I_{Max}$ (A)	$I$ (A)	$I_{Min}$ (A)	$I_{Max}$ (A)	$I$ (A)
Input current ripple in inductor 1	3.99	4.12	0.13	4.00	4.12	0.12
Input current ripple in inductor 2	4.01	4.13	0.12	4.04	4.15	0.11
Input current ripple in inductor 3	4.04	4.15	0.11	4.11	4.24	0.13
Input source current ripple	11.98	12.03	0.05	12.00	12.05	0.05

**Table 5.** Input current ripple of the 3-phase IBC connected to fuel cell and battery

Parameter (N=3)	Fuel cell			Battery		
	$I_{Min}$ (A)	$I_{Max}$ (A)	$I$ (A)	$I_{Min}$ (A)	$I_{Max}$ (A)	$I$ (A)
Input current ripple in Inductor 1	3.99	4.1	0.11	4.01	4.13	0.12
Input current ripple in Inductor 2	4.03	4.14	0.11	3.98	4.09	0.11
Input current ripple in Inductor 3	4.01	4.13	0.12	3.99	4.11	0.12
Input source current Ripple	11.94	11.98	0.04	11.97	12.01	0.04

This is done in such a way that at any time one of the inductors is supplying the load current. The frequency of ripple current in the output capacitor is n times compared to the single stage and therefore the value of the capacitor required can be reduced [87]. Also by virtue of paralleling the converters, the input current can be shared among the cells or phases [94], and reducing the overshoot [92] by controlling the switches ,conduction ( $I^2R$ ) losses,ripples,EMI can be reduced, thus increasing overall efficiency[90][95] . An active snubber circuit can be used to reduce the switching losses of 1- to N- phase interleaved dc-dc converter, extra current, voltage stresses and reverse recovery current[88][93].By doing so efficiency can be increased upto 97% compared to 2 and 3 phase converters. A four-phase interleaved boost converter with RES applications employing relatively low-valued inductive

and capacitive components is suitable for distributed power conversion, wide input voltage range applications [89-90].

**Table 6.** Output voltage ripple of the 3-phase IBC connected to RES's

Output Voltage Ripple (N=3)	$V_{Max}$ (V)	$V_{Min}$ (V)	$V$ (V)
SPV Panel	155.88	156.50	0.62
Wind Turbine Generator	155.73	156.35	0.62
Fuel cell	155.83	156.46	0.63
Battery	155.67	156.30	0.63



This topology along with the inter-phase coupled inductors can be used in order to improve transient response[91].Simulink model of the proposed 4-phase

IBC connected to AC grid and controlled by ANN as the local controller for voltage control, VDCC and current fine-tuning is shown in Fig.5

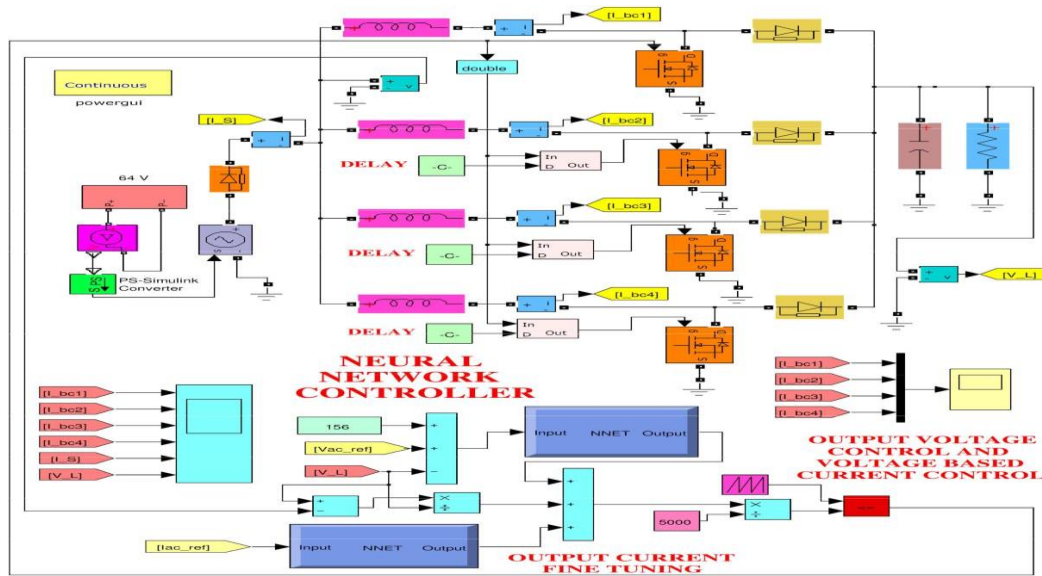


Fig.5. Simulink model of a 4-Phase IBC connected to SPV Panel

The ANN controller optimizes the duty cycle to restore the ' $V_{ref\_pcc}$ ' and to deliver ' $I_{ac\_ref}$ ' at the output of the IBC. The output of the ANN controller is in the form of a numeral and the PWM signal pertaining to that duty cycle is developed by comparing the ANN output with a triangular waveform of 5000Hz frequency using

relational operators. The developed pulse width modulation (PWM) signal is interleaved using 'discrete variable transport delay' to create a delay by an angle of  $90^\circ$  (time delay of 0.00005 second) in the PWM signals applied to each of the consecutive phases in an IBC which is shown in Fig.6

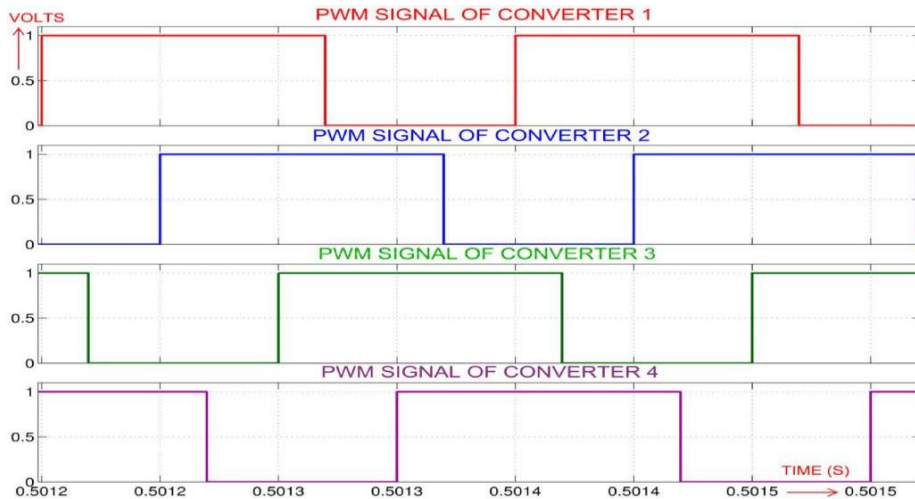


Fig.6. PWM signals applied to 4-Phase IBC

In case of the RE based HPS using the IBC as the basic converter topology, the instantaneous duty cycle of the IBC is mainly determined by the parameters such as the input voltage, the required output voltage, the instantaneous current needed to be delivered at the output of the IBC which is governed by the voltage control and VDCC technique. The input current ripple and the output voltage ripple of the IBC is also a function of duty cycle as seen in Fig.8

There exists a trade-off in estimating the duty cycle as the estimated duty cycle should complement all the desired task such as voltage control at PCC, VDCC and also the ripple reduction in input current and the output voltage.From the mathematical analysis performed, it is understood that in a 4-phase IBC, the input current and output voltage ripple is zero at the duty cycles 0.25, 0.5 and 0.75, and also the magnitude of the ripple around these duty cycle is negligible.With the primary objectives to reduce the ripple content and also to

maintain a constant output voltage (156V), the source voltage is carefully selected to operate the IBC at or nearer to 0.25, 0.5 or 0.75. The proposed 4-phase IBC proves to achieve a greater reduction in the output voltage and input

current ripple which can be revealed from Fig.7. which depicts the input current and the output voltage waveform of the IBC connected to SPV panel.

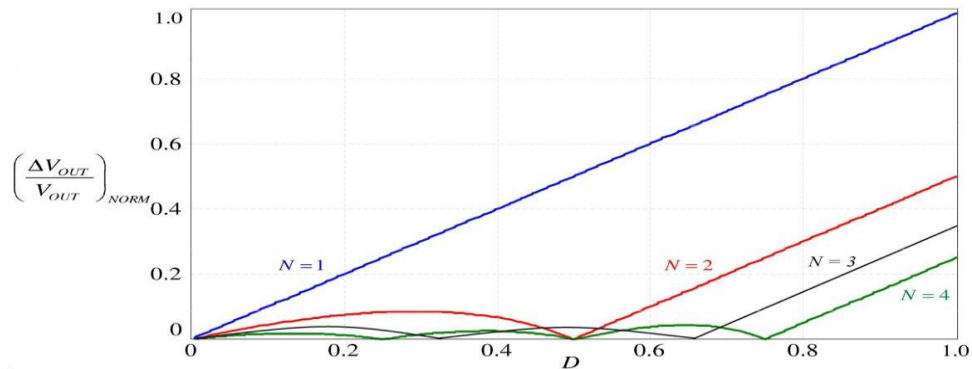


Fig.7. Input current and output voltage waveforms of the 4-phase IBC connected to SPV panel

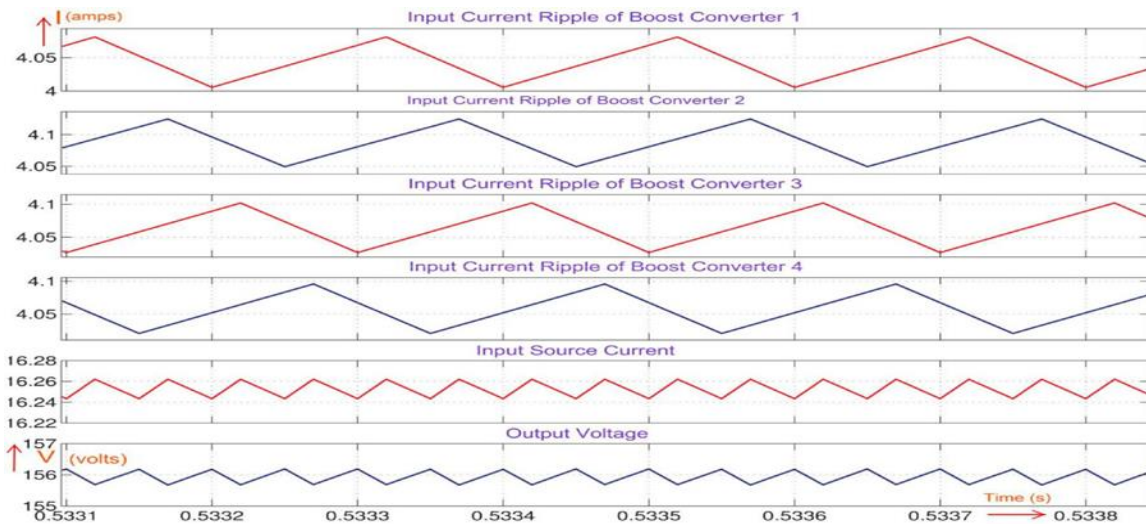


Fig.8. Input current ripple Vs duty cycle of an IBC based on number of phases

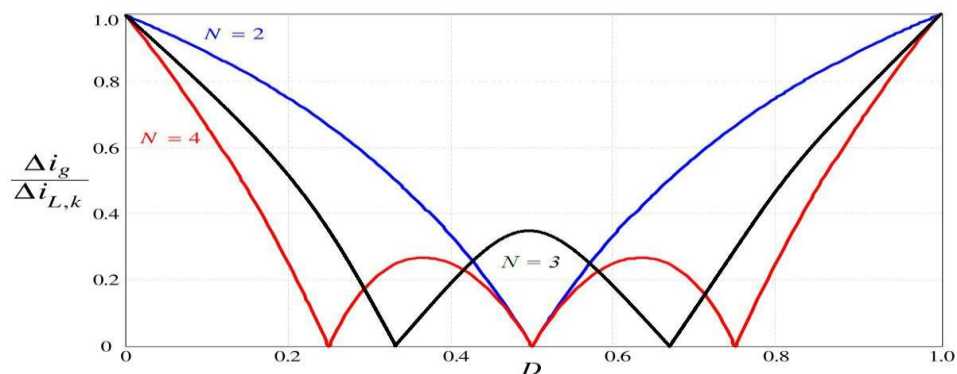


Fig.9. Normalized output voltage ripple ratio versus the duty cycle for different number of phases

An analysis on input current ripple and output voltage ripple is presented in Tables 7,8 &9 . It shows that the ripple in source current is significantly reduced to

0.02A by the 4-phase IBC which momentarily improves the overall efficiency of the conversion system.



**Table 7.** Input current ripple of the 4-phase IBC connected to SPV panel and WTG

Parameter (N=4)	SPV panel			WTG		
	$I_{Min}$ (A)	$I_{Max}$ (A)	$I$ (A)	$I_{Min}$ (A)	$I_{Max}$ (A)	$I$ (A)
Input current ripple in Inductor 1	4.002	4.085	0.083	4.121	4.050	0.071
Input current ripple in Inductor 2	4.045	4.130	0.085	4.128	4.055	0.073
Input current ripple in Inductor 3	4.022	4.106	0.084	4.110	4.036	0.071
Input current ripple in Inductor 4	4.017	4.100	0.083	4.095	4.021	0.074
Input source current Ripple	16.263	16.243	0.020	16.312	16.292	0.020

**Table 8.** Input current ripple of the 4-phase IBC connected to fuel cell and battery

Parameter (N=4)	Fuel cell			Battery		
	$I_{Min}$ (A)	$I_{Max}$ (A)	$I$ (A)	$I_{Min}$ (A)	$I_{Max}$ (A)	$I$ (A)
Input current ripple in Inductor 1	4.109	4.036	0.071	4.164	4.091	0.073
Input current ripple in Inductor 2	4.102	4.027	0.073	4.172	4.098	0.074
Input current ripple in Inductor 3	4.121	4.047	0.074	4.121	4.046	0.075
Input current Ripple in Inductor 4	4.086	4.012	0.074	4.112	4.038	0.074
Input source current Ripple	16.951	16.929	0.022	16.423	16.402	0.021

**Table 9.** Output voltage ripple of the 4-phase IBC connected to RES's

Output Voltage Ripple	$v_{Min}$ (V)	$v_{Max}$ (V)	$V$ (V)
SPV Panel	156.20	155.67	0.53
Wind Turbine Generator	156.26	155.73	0.53
Fuel cell	156.19	155.65	0.54
Battery	156.15	155.62	0.53

## 5. Observations and Conclusion

On detailed investigation carried out on literature on 2, 3, and 4 interleaved boost converterd with various

parameters of input current ripple , output voltage ripple it is understood that withincresae in number of phases the ripple can be reduced[Tab 10].

**Table 10.** Comparison of input current ripple and out voltage ripple in 2, 3 & 4 Phase IBC's

Number of phase	Input source current Ripple				Output Voltage Ripple			
	SPV Panel	WTG	Fuel Cell	Battery	SPV Panel	WTG	Fuel Cell	Battery
2	0.08	0.09	0.08	0.08	0.80	0.85	0.86	0.84
3	0.05	0.05	0.04	0.04	0.62	0.62	0.63	0.63
4	0.02	0.020	0.022	0.021	0.53	0.53	0.54	0.53

From the analysis, it is evident that the proposed 4-phase IBC offers a good reduction of ripple in the inputcurrent and in output voltage than that of the 2-phase, 3-phase IBC and conventional boost converter's the results and observations documented infer that with increases in number of phases, the ripple can be brought down. However, the challenges left are with the gate signals complexity with increase in number of phases. Investigations are currently in progress to identify the optimized number of phases in IBC with limited complexity in gate circuits.

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